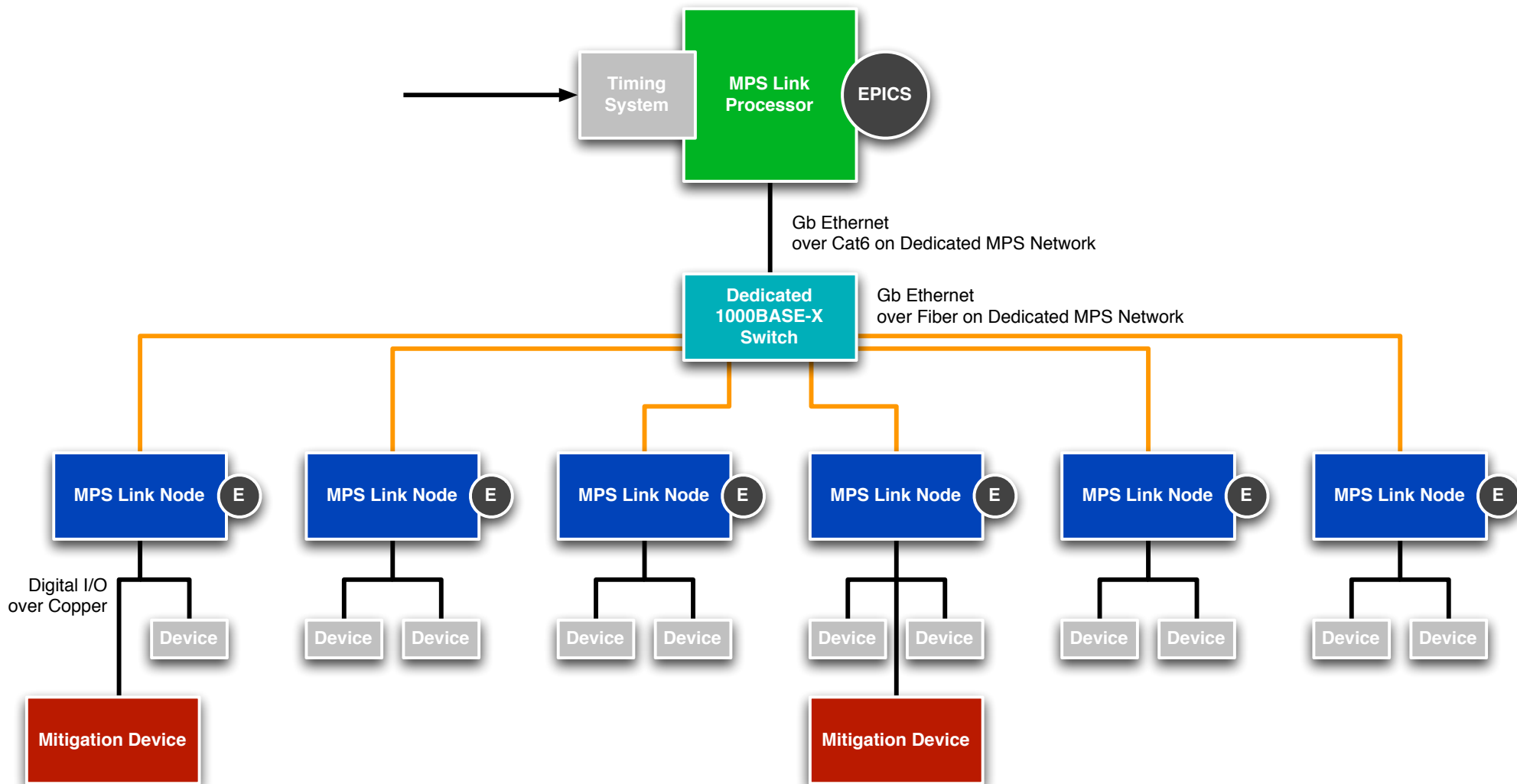


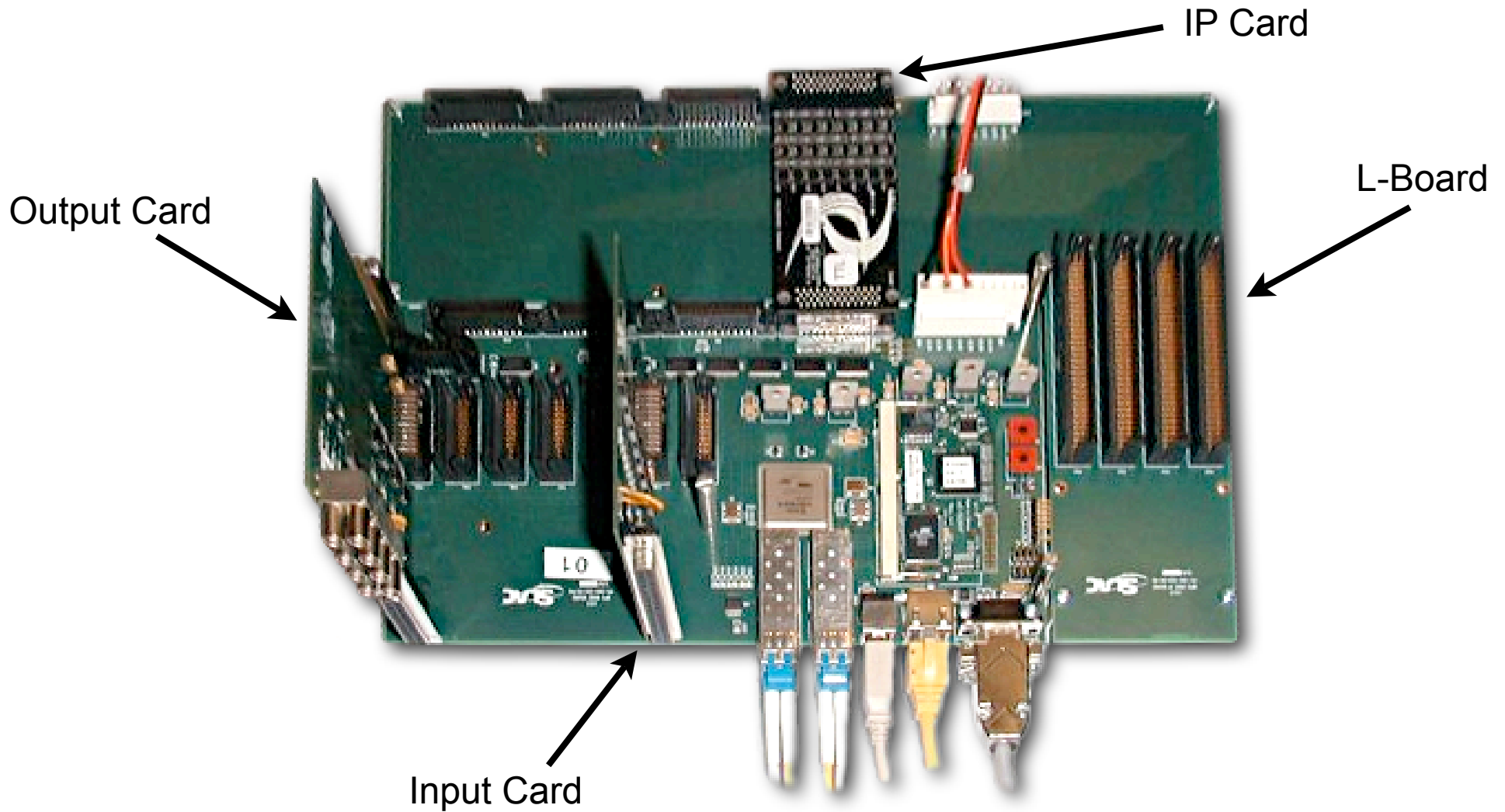
Beam Loss Monitors and MPS

- Link Node overview
- BLM issues

LCLS MPS Overview



MPS Link Node



Link Node Status

- Link Node is built
- Preliminary testing has begun
 - Communication with Link Processor works
 - Fault latching and reporting works
- Field test begins next week

Link Node/BLM Status

- Link Node IP card interface implemented
- SLAC ADC IP card designed
 - Reads PMT signals
 - Provides binary fault signals to MPS
- High Voltage Power Supply
 - Control by COTS IP DAC
 - Readback by COTS IP ADC
 - These IP cards are used by other systems in the LCLS

BLM Schedule

BLM Schedule FY 2008	march	april	may	june	july	aug	sept	oct
detector design lock	x							
assembly drawing set		x						
procurements		x	x					
detector fabrication			x	x	x	x		
detector assembly						x	x	
ship to slac							x	
installation								x

BLM-Link Node Integration

- APS detector and interface box prototypes to arrive at SLAC in one month
- Install link node, and prototype detector in the LCLS injector
- Resolve integration issues before installation

BLM Installation

- Five detectors to be installed as soon as we get them from the APS, sometime in October
- Installation should be simple