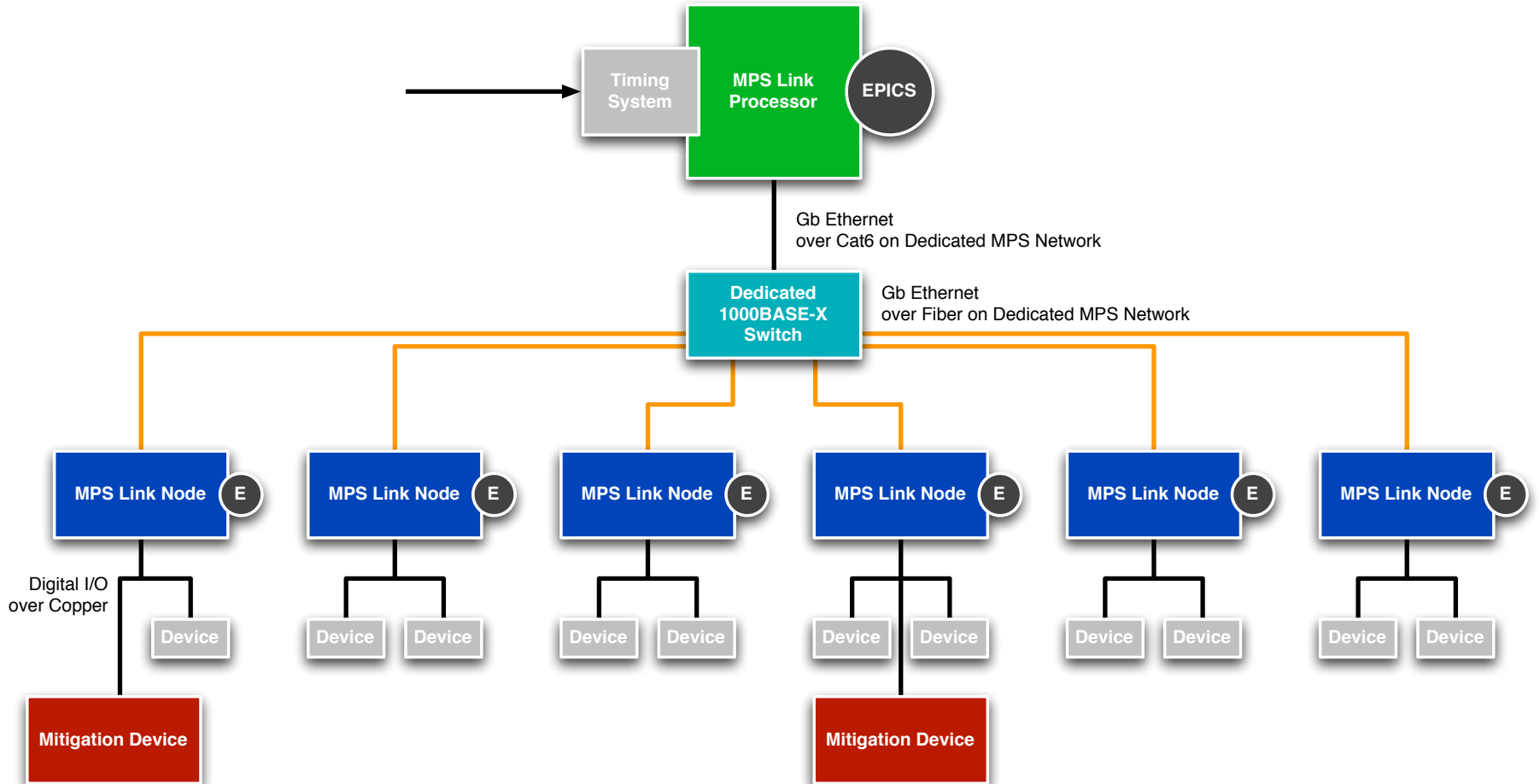


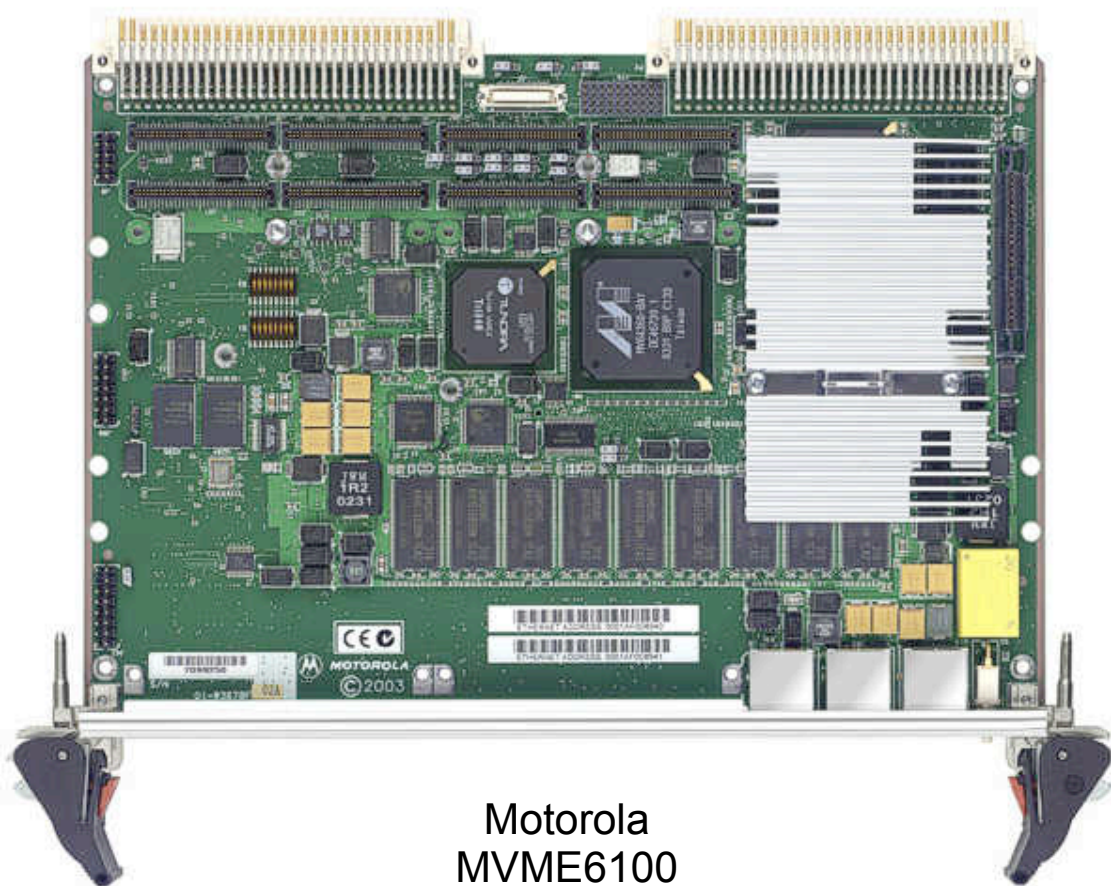
Machine Protection System

- Overview
- Link Processor
- Link Node
- Beam Loss Monitors

LCLS MPS Overview



MPS Link Processor

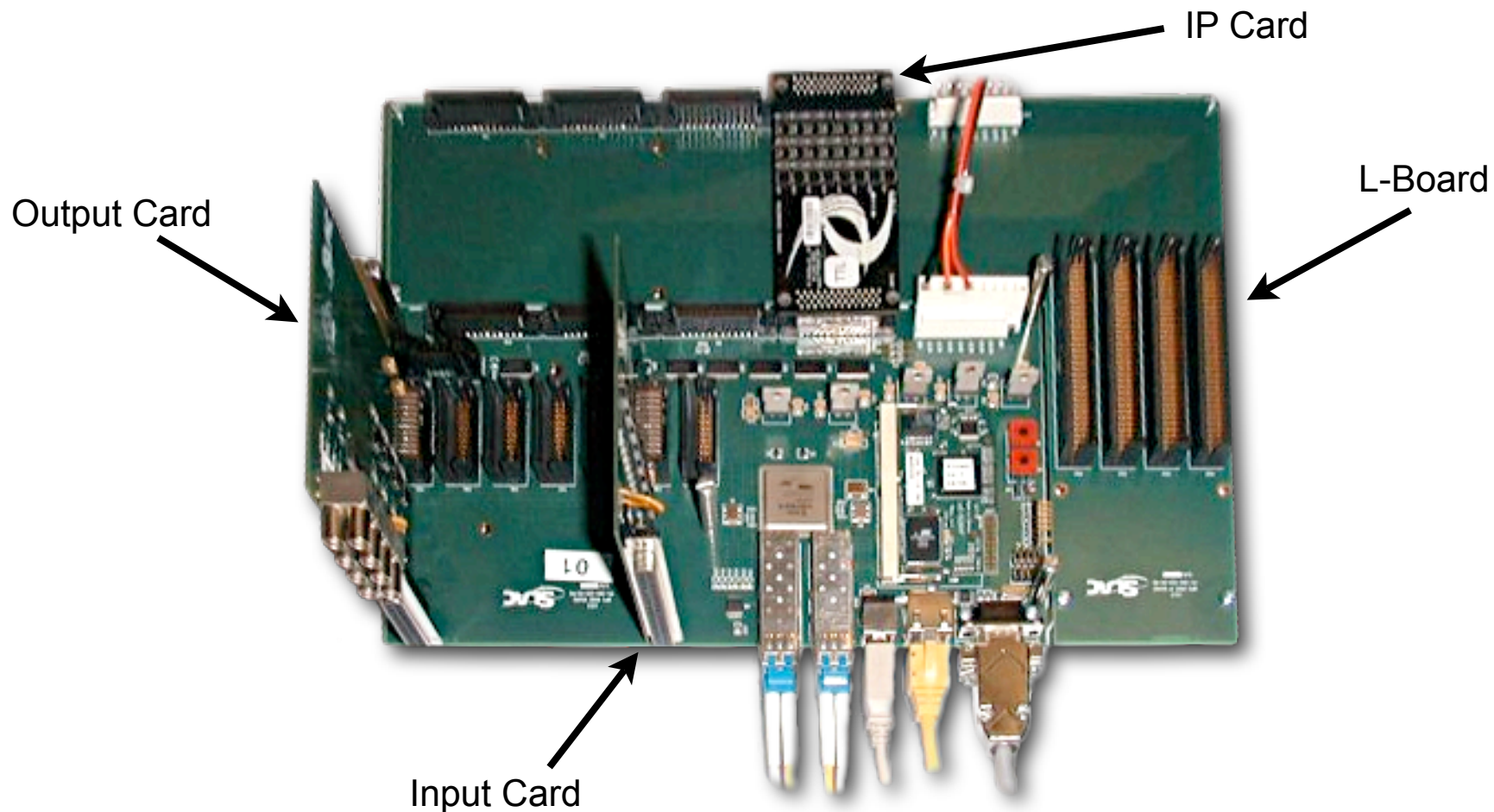


Motorola
MVME6100

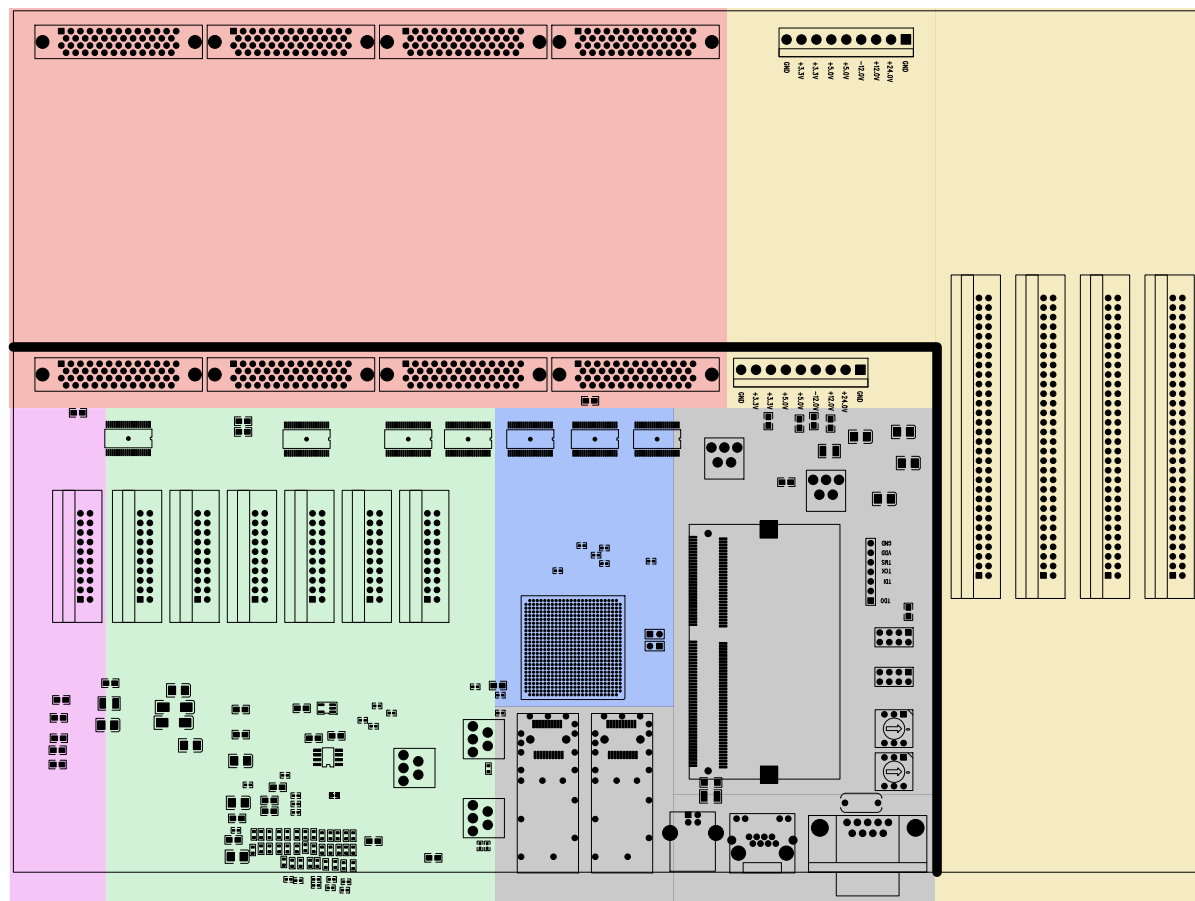


Micro-Research
PMC-EVR-200

MPS Link Node

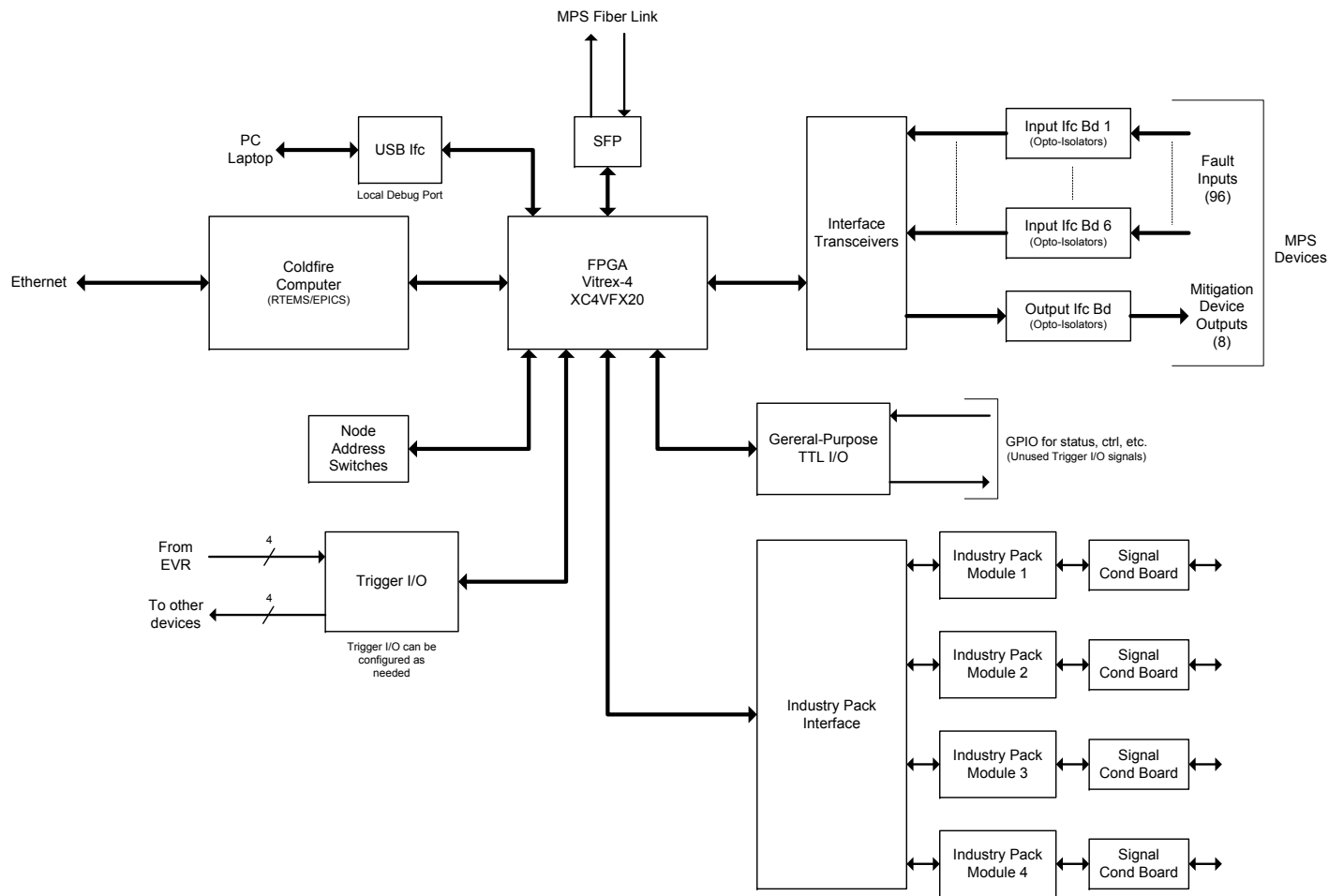


Link Node PCB



- Output Card
- Input Cards
- FPGA
- ColdFire/Network
- IP Cards
- IP Signal Cond.

Link Node Block Diagram



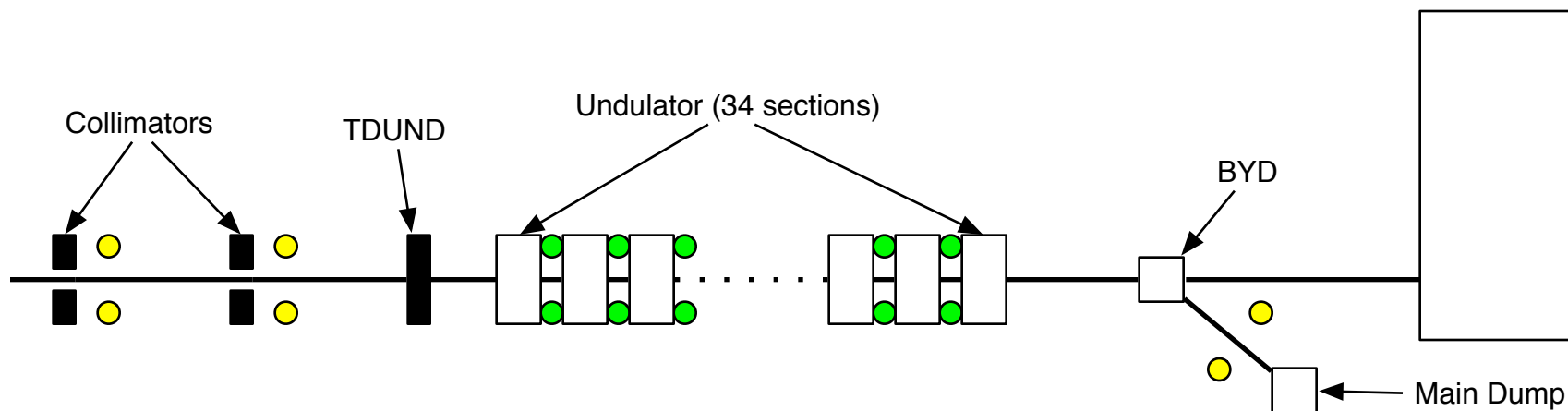
Link Processor Status

- Major software modules have been coded
- Code review was held this month
- Network communication tested to emulated Link Node

Link Node Status

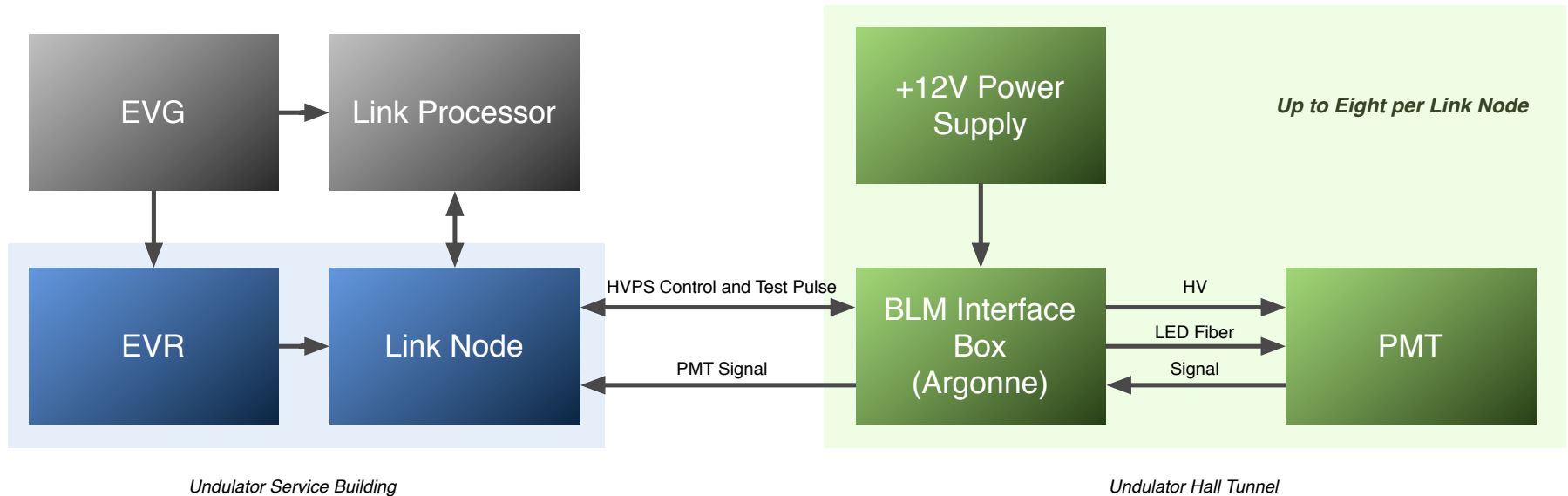
- Prototypes are created, being tested
- FPGA firmware is being written
- Network communication has been tested
- PCB relay layout is underway

Undulator Beam Loss Monitors



- Undulator PMT
- Protection Ion Chamber

Beam Loss Monitors



- One EVR trigger per Link Node
- One Link Node per eight beam loss monitors

Beam Loss Monitor Status

- ADC Schematic is complete
- Preparing ADC PCB layout
- FGPA design has started
- Nearly completed schematic of BLM Interface Board
- L-Board schematic has been started